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INFORMATION DISCOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known				
Application Number	10/083,241			
Filing Date	February 25, 2002			
First Named Inventor	Yervant Zorian			
Art Unit	2133			
Examiner Name	Ton, David			
Attorney Docket Number				

		Document Number		NT DOCUMENTS		
Examiner Initials*	Cite No.'	Number - Kind Code ¹ (if known)	Publication Data or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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1		US-6,304,989	10-16-2001	Kraus et al.		
		US-6,408,401	06-18-2002	Bhavsar et al.		
		US-6,067,262	05-23-2000	Irrinki et al.		
		US-5,583,463	12-10-1996	Merritt		
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FOREIGN PATENT DOCUMENTS							
Examiner Cite No.'	Cite	Foreign Patent Document			Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	No.1	Country Code* - Number* - Kind Code* (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document			
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Based on PTO/SB/08A (08-03) as modified by Blakely, Sciokoff, Taylor & Zafman (wtr) 08/11/2003.

^{*}Examiner: Indias if reference considered, whether or not citation is in conformance with MPEP 609. Oraw line through citation if not in conformance and not considered. Include copy of this form with next communication.

^{&#}x27;Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. 'Enter Office that issued the document, by the two-latter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

of

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Complete if Known

Application Number 10/083,241

Filing Date February 25, 2002

First Named Inventor Yervant Zorian

Art Unit 2133

Examiner Name Ton, David

Attorney Docket Number 4640P006

		NON PATENT LITERATURE DOCUMENTS					
Examiner Initials* City No.		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		TSIN-YUANCHANG et al., "Tutorial 2: SoC Testing and P1500 Standard", Asian Test Symposium 2000: 492					
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		ALFREDO BENSO et al., "HD-BIST: A Hierarchical Framework for BIST Scheduling and Diagnosis in SOCS", ITC 1999: 1038-1044					
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		YERVANT ZORIAN et al., "Embedded-Memory Test and Repair: Infrastructure IP for SoC Yield", IEEE CS and IEEE CASS May-June 2003: 58-66					
		YERVANT ZORIAN et al., "Embedding Infrastructure IP for SoC Yield Improvement", June 2002, pp. 709-712.					
01		PRAVEEN PARVATHALA et al., "FRITS-A Microprocessor Functional BIST Method", March 2002, pp. 590-598					

	Examiner Signature		Date Considered	120/2005
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